

# Abstract

A cell re-sequencer restores cell sequence in  
multipath ATM switches by using per-VC logical queues that  
5 store only cells belonging to a same VC. The re-sequencer  
can reduce processing time to a shorter value than those of  
conventional re-sequencer mechanisms. Also, this re-  
sequencer has no restriction on the peak rate of VCs and no  
arbitration functions to select an output cell. The re-  
10 sequencer comprises a RAM buffer, a CAM /RAM table, a  
controller, etc.